

Day : Tuesday
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Inventor Name Search Result

Your Search was:

Last Name = KAMIYA

First Name = SATOSHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08798906</u>	<u>5974232</u>	150	02/12/1997	IMAGE PROCESSING APPARATUS THAT EXECUTES ABORTION OF IMAGE PROCESSING AND METHOD OF RESUMING ABORTED IMAGE PROCESSING	KAMIYA, SATOSHI
<u>08813429</u>	<u>5940368</u>	150	03/10/1997	CELL RATE SUPERVISING SYSTEM TERMINATING CONGESTION FEEDBACK LOOP	KAMIYA, SATOSHI
<u>08815400</u>	<u>5974033</u>	150	03/10/1997	DYNAMIC SHAPING APPARATUS OF TRAFFIC OF ATM NETWORK	KAMIYA, SATOSHI
<u>08837102</u>	<u>5886981</u>	150	04/14/1997	CELL OUTPUT CONTENTION CONTROL APPARATUS FOR TRAFFIC SHAPER	KAMIYA, SATOSHI
<u>08896026</u>	<u>6005843</u>	150	07/17/1997	VIRTUAL SOURCE/VIRTUAL DESTINATION DEVICE OF ASYNCHRONOUS TRANSFER MODE NETWORK	KAMIYA, SATOSHI
<u>09164599</u>	<u>6438138</u>	150	10/01/1998	BUFFER CONTROLLER INCORPORATED IN ASYNCHRONOUS TRANSFER MODE NETWORK FOR CHANGING TRANSMISSION CELL RATE DEPENDING ON DURATION OF CONGESTION AND METHOD FOR CONTROLLING THEREOF	KAMIYA, SATOSHI
<u>09262825</u>	<u>6704321</u>	150	03/05/1999	TRAFFIC SHAPER	KAMIYA, SATOSHI
<u>09511905</u>	<u>6801498</u>	150	02/23/2000	ASYNCHRONOUS TRANSFER MODE COMMUNICATION EQUIPMENT AND METHOD FOR SWITCHING VIRTUAL PATH OF SAME	KAMIYA, SATOSHI
<u>09734695</u>	<u>6885639</u>	150	12/13/2000	PACKET SWITCH AND PACKET SWITCHING METHOD	KAMIYA, SATOSHI

<u>09814714</u>	<u>7142546</u>	150	03/23/2001	DISTRIBUTED PIPELINE SCHEDULING METHOD AND SYSTEM	KAMIYA, SATOSHI
<u>09966099</u>	<u>6977935</u>	150	10/01/2001	TWO-DIMENSIONAL PIPELINED SCHEDULING TECHNIQUE	KAMIYA, SATOSHI
<u>10022594</u>	Not Issued	71	12/20/2001	Apparatus and method for GFP frame transfer	KAMIYA, SATOSHI
<u>10024144</u>	Not Issued	161	12/21/2001	Apparatus and method for GFP frame transfer	KAMIYA, SATOSHI
<u>10074015</u>	Not Issued	41	02/14/2002	Packet switch	KAMIYA, SATOSHI
<u>10316840</u>	<u>7042904</u>	150	12/12/2002	METHOD AND APPARATUS FOR TRANSMITTING MULTIPLE SIGNAL, METHOD AND APPARATUS FOR RECEIVING MULTIPLE SIGNAL, MULTIPLE SIGNAL TRANSMISSION METHOD AND MULTIPLEXER/DEMULTIPLEXER	KAMIYA, SATOSHI
<u>10385514</u>	Not Issued	41	03/12/2003	Multiplex transmission system for reporting alert information to the conversion device on the receiving side without using a dedicated frame	KAMIYA, SATOSHI
<u>10390471</u>	Not Issued	71	03/17/2003	Multiplex transmission system capable of using ordinary network packets to transmit a plurality of 8B/10B bit streams	KAMIYA, SATOSHI
<u>10546718</u>	Not Issued	30	08/24/2005	Packet relay device and packet method, and program	KAMIYA, SATOSHI
<u>10908086</u>	Not Issued	30	04/27/2005	DRIVE CONTROL DEVICE FOR ELECTRIC VEHICLE	KAMIYA, SATOSHI
<u>10996403</u>	Not Issued	30	11/26/2004	Device, method and program for band control	KAMIYA, SATOSHI
<u>11160656</u>	<u>7119506</u>	150	07/04/2005	DRIVE CONTROLLING DEVICE OF A MOTOR-DRIVEN VEHICLE	KAMIYA, SATOSHI
<u>11255173</u>	<u>7161943</u>	150	10/21/2005	TWO-DIMENSIONAL PIPELINED SCHEDULING TECHNIQUE	KAMIYA, SATOSHI
<u>11519228</u>	Not Issued	25	09/12/2006	Event processing method in a computer system	KAMIYA, SATOSHI
<u>11523346</u>	Not Issued	30	09/19/2006	Maintenance system for image processing device, image processing device, maintenance information display method for image processing device, storage medium, and computer	KAMIYA, SATOSHI

				data signal	
<u>11562923</u>	Not Issued	30	11/22/2006	TWO-DIMENSIONAL PIPELINED SCHEDULING TECHNIQUE	KAMIYA, SATOSHI
<u>11620419</u>	Not Issued	30	01/05/2007	DATA PROCESSING DEVICE AND DATA PROCESSING METHOD	KAMIYA, SATOSHI
<u>07490774</u>	Not Issued	166	03/08/1990	PROCESS FOR DEPOSITING TANTALUM OXIDE FILM AND CHEMICAL VAPOR DEPOSITION SYSTEM USED THEREFORE	KAMIYAMA, SATOSHI
<u>07650695</u>	<u>5082162</u>	250	02/05/1991	MATERIAL AND METHODS FOR SOLDERING SEMICONDUCTOR DEVICES	KAMIYAMA, SATOSHI
<u>07650981</u>	Not Issued	161	02/04/1991	PROCESS FOR DEPOSITING TANTALUM OXIDE FILM AND CHEMICAL VAPOR DEPOSITION SYSTEM USED THEREFORE	KAMIYAMA, SATOSHI
<u>07753765</u>	<u>5254505</u>	250	09/03/1991	PROCESS OF FORMING CAPACITIVE INSULATING FILM	KAMIYAMA, SATOSHI
<u>07756016</u>	<u>5185755</u>	250	09/06/1991	SEMICONDUCTOR LASER	KAMIYAMA, SATOSHI
<u>07894938</u>	<u>5202280</u>	250	06/08/1992	METHOD FOR FABRICATION A SEMICONDUCTOR DEVICE	KAMIYAMA, SATOSHI
<u>07945260</u>	<u>5279985</u>	250	09/15/1992	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF	KAMIYAMA, SATOSHI
<u>07953359</u>	<u>5323405</u>	250	09/30/1992	ANTI-GUIDED PHASE-LOCKED ARRAY AND MANUFACTURING METHOD THEREFOR	KAMIYAMA, SATOSHI
<u>07991223</u>	<u>5268328</u>	250	12/16/1992	METHOD OF FABRICATING A SEMICONDUCTOR LASER	KAMIYAMA, SATOSHI
<u>08023627</u>	Not Issued	161	02/26/1993	FABRICATION METHOD OF CAPACITOR USED FOR INTEGRATED CIRCUITS	KAMIYAMA, SATOSHI
<u>08092358</u>	<u>5383214</u>	250	07/15/1993	SEMICONDUCTOR LASER AND A METHOD FOR PRODUCING THE SAME	KAMIYAMA, SATOSHI
<u>08102634</u>	<u>5438012</u>	150	08/05/1993	METHOD OF FABRICATING CAPACITOR ELEMENT IN SUPER-LSI	KAMIYAMA, SATOSHI
<u>08196486</u>	<u>5352623</u>	150	02/15/1994	METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE	KAMIYAMA, SATOSHI
<u>08223215</u>	<u>5600667</u>	150	04/05/1994	SEMICONDUCTOR LASER DEVICE	KAMIYAMA, SATOSHI
<u>08278395</u>	<u>5523256</u>	150	07/21/1994	METHOD FOR PRODUCING A	KAMIYAMA,

				SEMICONDUCTOR LASER	SATOSHI
08347712	5486488	150	12/01/1994	PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE	KAMIYAMA, SATOSHI
08353204	5508221	150	12/01/1994	METHOD FOR FORMING CAPACITOR ELEMENT OF DRAM	KAMIYAMA, SATOSHI
08378607	Not Issued	161	01/26/1995	FABRICATION PROCESS OF SEMICONDUCTOR DEVICE	KAMIYAMA, SATOSHI
08396506	5502739	150	03/01/1995	SEMICONDUCTOR LASER DEVICE	KAMIYAMA, SATOSHI
08396509	6055253	150	03/01/1995	A SEMICONDUCTOR LASER DEVICE WITH AN OPTICAL GUIDE LAYER	KAMIYAMA, SATOSHI
08396521	Not Issued	161	03/01/1995	SEMICONDUCTOR LASER DEVICE	KAMIYAMA, SATOSHI
08415540	5705831	250	04/03/1995	SEMICONDUCTOR LIGHT-EMITTING DEVICE AND PRODUCTION METHOD THEREOF, AND CRYSTAL-GROWING METHOD SUITABLE FOR THE PRODUCTION METHOD	KAMIYAMA, SATOSHI
08523189	5619520	250	09/05/1995	SEMICONDUCTOR LASER	KAMIYAMA, SATOSHI
08550355	5622888	250	10/30/1995	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE	KAMIYAMA, SATOSHI

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Inventor Name Search Result

Your Search was:

Last Name = NISHIHARA

First Name = MOTOO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07931039</u>	<u>5469543</u>	150	08/14/1992	POLICING CIRCUITS ARRANGED IN MATRIX ARRAY FOR SELECTIVELY TRANSFERRING VIRTUAL PATH IDENTIFIER (VPI) RESPONSIVE TO EITHER VPI OR SERVICE CLASS IDENTIFIER (SCI) THRESHOLD VALUE	NISHIHARA, MOTOO
<u>07964135</u>	<u>5450440</u>	150	10/22/1992	MONITOR SYSTEM FOR DIGITAL COMMUNICATION APPARATUS	NISHIHARA, MOTOO
<u>07975377</u>	<u>5337307</u>	150	11/13/1992	METHOD OF TRACING VIRTUAL PATH OPERATION INFORMATION AND APPARATUS APPLIED THERE TO	NISHIHARA, MOTOO
<u>08015343</u>	<u>5394408</u>	150	02/08/1993	POLICING CONTROL APPARATUS	NISHIHARA, MOTOO
<u>08015651</u>	<u>5414415</u>	150	02/09/1993	CROSS-CONNECT APPARATUS CAPABLE OF AVOIDING A SUPERFLUOUS DETOUR ROUTE THEREIN	NISHIHARA, MOTOO
<u>08647040</u>	<u>5764637</u>	150	05/09/1996	STM/ATM CONVERTER	NISHIHARA, MOTOO
<u>08655216</u>	<u>5742600</u>	150	06/05/1996	MULTIPLEX ATM/STM CONVERTER FOR STRUCTURED DATA	NISHIHARA, MOTOO
<u>08910117</u>	<u>6201810</u>	150	08/13/1997	HIGH-SPEED ROUTING CONTROL SYSTEM	NISHIHARA, MOTOO
<u>08912188</u>	<u>6137798</u>	150	08/15/1997	CONNECTIONLESS NETWORK FOR ROUTING CELLS WITH CONNECTIONLESS	NISHIHARA, MOTOO

				ADDRESS, VPI AND PACKET-IDENTIFYING VCI	
<u>09039556</u>	<u>6208653</u>	250	03/16/1998	METHOD AND APPARATUS FOR CONTROLLING A FLOW BETWEEN TERMINALS IN AN ATM NETWORK	NISHIHARA, MOTOO
<u>09075843</u>	<u>6314098</u>	150	05/12/1998	ATM CONNECTIONLESS COMMUNICATION SYSTEM HAVING SESSION SUPERVISING AND CONNECTION SUPERVISING FUNCTIONS	NISHIHARA, MOTOO
<u>09114135</u>	<u>6330242</u>	150	07/13/1998	LOOSE SOURCE ROUTING METHOD OF IP PACKET ON ATM NETWORK	NISHIHARA, MOTOO
<u>09157471</u>	<u>6424620</u>	150	09/21/1998	CONGESTION CONTROL SYSTEM CAPABLE OF DETECTING CONGESTION OCCURRING IN AN ASYNCHRONOUS TRANSFER MODE NETWORK AND AUTONOMOUSLY AVOIDING THE CONGESTION	NISHIHARA, MOTOO
<u>09184513</u>	<u>6580714</u>	150	11/02/1998	CONCENTRATOR TYPE ATM SWITCH FOR AN ATM SWITCHING SYSTEM	NISHIHARA, MOTOO
<u>09216943</u>	<u>6452908</u>	150	12/21/1998	ROUTE SEARCHING CIRCUIT AND COMMUNICATION APPARATUS USING THE THE SAME	NISHIHARA, MOTOO
<u>09345770</u>	<u>6546422</u>	150	07/01/1999	CACHING OF NETWORK CONTENTS BY PACKET RELAYS THAT DETERMINE CACHE PRIORITY UTILIZING CONTENTS ACCESS FREQUENCY AND METRICS IN THEIR ROUTING TABLES REPRESENTING RELAYING PATH LENGTHS	NISHIHARA, MOTOO
<u>09447580</u>	<u>6512616</u>	150	11/23/1999	OPTICAL PACKET SWITCH	NISHIHARA, MOTOO
<u>09511773</u>	<u>6738352</u>	150	02/24/2000	TRANSFER DESTINATION DETERMINING PROCESS APPARATUS	NISHIHARA, MOTOO
<u>09534294</u>	<u>6678474</u>	150	03/23/2000	LIGHTWAVE NETWORK DATA COMMUNICATIONS SYSTEM	NISHIHARA, MOTOO

<u>09580559</u>	<u>6970478</u>	150	05/30/2000	PACKET TRANSFER METHOD AND APPARATUS, AND PACKET COMMUNICATION SYSTEM	NISHIHARA, MOTOO
<u>09638878</u>	<u>6754215</u>	150	08/15/2000	PACKET SCHEDULING DEVICE	NISHIHARA, MOTOO
<u>09669565</u>	<u>6947427</u>	150	09/26/2000	TRANSMISSION METHOD AND NETWORK SYSTEM FOR ACCOMMODATING A PLURALITY OF KINDS OF TRAFFIC IN A COMMON NETWORK	NISHIHARA, MOTOO
<u>09733940</u>	<u>7050455</u>	150	12/12/2000	FRAME CONSTRUCTION METHOD, FRAME CONSTRUCTION DEVICE AND DATA TRANSFER SYSTEM CAPABLE OF ACCOMMODATING STM TRAFFIC AND BEST EFFORT TRAFFIC IN COMMON FRAME FORMAT	NISHIHARA, MOTOO
<u>09752461</u>	<u>6940853</u>	150	01/03/2001	DATAGRAM RELAYING APPARATUS WITH LOAD DISTRIBUTING FUNCTION	NISHIHARA, MOTOO
<u>09782879</u>	<u>7126950</u>	150	02/13/2001	METHOD AND SYSTEM FOR TRANSMISSION AND RECEPTION OF ASYNCHRONOUSLY MULTIPLEXED SIGNALS	NISHIHARA, MOTOO
<u>09864300</u>	<u>6976256</u>	150	05/25/2001	PIPELINE PROCESSING TYPE SHAPING APPARATUS AND ITS METHOD	NISHIHARA, MOTOO
<u>09925707</u>	Not Issued	61	08/10/2001	Device, method and system for transferring frame	NISHIHARA, MOTOO
<u>10022594</u>	Not Issued	71	12/20/2001	Apparatus and method for GFP frame transfer	NISHIHARA, MOTOO
<u>10024144</u>	Not Issued	161	12/21/2001	Apparatus and method for GFP frame transfer	NISHIHARA, MOTOO
<u>11185023</u>	Not Issued	30	07/20/2005	Datagram relaying apparatus with load distributing function	NISHIHARA, MOTOO
<u>11326437</u>	Not Issued	30	01/06/2006	Frame construction method, frame construction device and data transfer system capable of accommodating STM traffic and best effort traffic in common frame format	NISHIHARA, MOTOO

<u>11414415</u>	Not Issued	25	05/01/2006	Method and system for transmission and reception of asynchronously multiplexed signals	NISHIHARA, MOTOO
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Inventor Name Search Result

Your Search was:

Last Name = IKEMATSU

First Name = RYUICHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07965886</u>	Not Issued	166	10/23/1992	TEST METHOD AND APPARATUS CAPABLE OF TETING RECEPTION OF A DESIRED CELL THROUGH A VIRTUAL PATH	IKEMATSU, RYUICHI
<u>08017744</u>	5359600	150	02/16/1993	HIGH THROUGHPUT SUPERVISORY SYSTEM FOR ATM SWITCHING SYSTEMS TRANSPORTING STM-N SIGNALS	IKEMATSU, RYUICHI
<u>08259120</u>	5440565	150	06/13/1994	TEST METHOD AND APPARATUS CAPABLE OF TESTING RECEPTION OF A DESIRED CELL THROUGH A VIRTUAL PATH	IKEMATSU, RYUICHI
<u>09197680</u>	6535479	150	11/23/1998	HITLESS SWITCHING SYSTEM OF ATM SWITCH APPARATUS IN WHICH DISCARD PRIORITY CONTROL IS STOPPED	IKEMATSU, RYUICHI
<u>09266772</u>	6490282	150	03/12/1999	SWITCHING SYSTEM FOR ASYNCHRONOUS TRANSFER MODE SWITCH	IKEMATSU, RYUICHI
<u>09676815</u>	6731636	150	10/02/2000	SCHEDULER USING SMALL SIZED SHUFFLE PATTERN IN ATM NETWORK	IKEMATSU, RYUICHI
<u>09779744</u>	6934295	150	02/09/2001	MULTI-MODE SCHEDULER, APPARATUS INCLUDING MULTI-MODE SCHEDULER AND MULTI-MODE SCHEDULING METHOD	IKEMATSU, RYUICHI
<u>10022594</u>	Not Issued	71	12/20/2001	Apparatus and method for GFP frame transfer	IKEMATSU, RYUICHI
<u>10245375</u>	6756898	150	09/18/2002	NETWORK TERMINATION	IKEMATSU,

				DEVICE, ALARM TRANSFER SYSTEM AND ALARM TRANSFERRING METHOD	RYUICHI
<u>11676183</u>	Not Issued	25	02/16/2007	SIMULATION METHOD FOR IMPROVING FREEDOM OF SETTING PARAMETERS RELATING TO INPUT/OUTPUT CHARACTERISTICS OF A MEMORY CHIP	IKEMATSU, RYUICHI

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